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Spec update provides specification changes, errata, specification clarifications, and document changes to the 8th and 9th Generation Intel® Core™ processor family datasheet for the S/H/U-processor line.

8th and 9th Gen
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Processor Family
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For the Intel®
82801IB ICH9,
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82801IH ICH9DH,
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March 2011 This document is an update to the specifications contained in the Affected Documents/ Related Documents... table below. This document is a compilation of device and documentation errata, specification clarifications and changes.

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Specification

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Spec update:

Provides datasheet
specification errata,
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changes and
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Intel® Celeron® processor family datasheet and the Mobile 4th Generation Intel® Core™ processor family (U and Y-processor lines), Desktop Intel® Pentium® processor family, and Desktop Intel® Celeron® ...

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For the Intel®82Q45, 82Q43, 82B43, 82G45, 82G43, 82G41 Graphics and Memory Controller Hub (GMCH) and the Intel®82P45, 82P43 Memory Controller Hub (MCH) July 2010.

Document Number:
319971-014.

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Intel® 4 Series
Chipset Family
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Intel® Xeon®
Processor 5600
Series Specification
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This document is an update to the specifications contained in the Affected Documents table. This document is a compilation of device and documentation errata, specification clarifications, and changes.

Intel® Xeon®

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Processor 5600
Series Specification
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Features;

Functionality; High-
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Size: 4.96 MB Date:

March 2020 Revision:

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Update (PDF) The
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R1000SPO families
describes: Changes;
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Revision: 1.3

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identification information table that lists these QDF numbers and the corresponding product sample details. Specification Changes are modifications to the current published specifications. These changes will be incorporated in the next release of the

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specifications

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2 Specification

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6th Generation Intel
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Page 1 Specification
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definition not a part of an application, but rather a supplemental piece of software, it can make a fundamental difference during the development of an application. Such tools aid application developers in the context of debugging, performance analysis, and code

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optimization, and therefore make a major contribution to the development of robust and efficient parallel software. This book introduces a selection of the tools presented and discussed at the 9th International Parallel Tools Workshop held in Dresden, Germany, September 2-3, 2015,

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provided for a panel on certifying and assessing critical systems. Among the specific topics are building fault-tolerant hardware clocks from commercial components, improving the performance of atomic broadcast protocols using the newsmonger

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technique, the experimentally validating high-speed systems using physical fault injection, and evaluating dependability using a multi-criteria decision analysis procedure. No mention is made of where or when the conference was held. There is no subject

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